

■ **CLASS DISCUSSION ITEM 9.2**

***LVDT Demodulation***

Trace the currents through the diodes in the demodulation circuit shown in Figure 9.7 for different core positions (null, left of null, and right of null) and explain why the output voltage behaves as shown. Assume ideal diodes. Also, explain why the output is 0 when the core is in the null or center position.